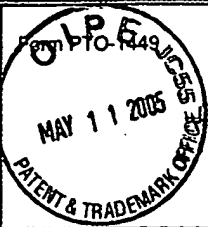


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		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-2358		SERIAL NO. 10/624,627		
		LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)						
		APPLICANT: Luan C. Tran		FILING DATE July 21, 2003		GROUP 2812		
U.S. PATENT DOCUMENTS								
Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate	
Ju	AA	5,534,456	07/96	Yuan et al.				
Ju	AB	5,970,352	10/99	Shiozawa et al.				
Ju	AC	6,001,707	12/99	Lin et al.				
Ju	AD	6,420,749	07/02	Divakaran et al.				
Ju	AE	6,187,624	02/01	Huang				
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Ju	AG	6,242,329	06/01	Huster et al.				
Ju	AH	5,904,530	05/99	Shin				
Ju	AI	US2002/0006693	01/02	Matsuda				
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
Ju	AJ	JP04105328A	04/92	Japan				
	AK							
	AL							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
Ju	AM		Wolf et al., <i>Silicon Processing for the VLSI Era</i> , Vol. 1: "Process Technology," Second Edition, Lattice Press 2000, pp. 12-13, 25-27.					
Ju	AN		S.M. Sze, <i>VLSI Technology</i> , Second Edition, McGraw-Hill, 1988, pages 472-483					
	AO							
EXAMINER		Jennifer M. Kennedy July 22, 2005						
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								

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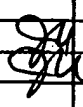
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1

of 2

Application Number	10/824,627
Filing Date	07/21/2003
First Named Inventor	Luan C. Tran
Art Unit	2812
Examiner Name	J. Kennedy
Attorney Docket Number	MI22-2358

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.†	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code* (if known)			
		US- 5,532,107	7/1996	Ole et al.	
		US- 2001/0006752	7/2001	Watanabe et al.	
		US-			
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete If Known		
		Application Number	10/824,627	
		Filing Date	July 21, 2003	
		First Named Inventor	Luan C. Tran	
		Art Unit	2812	
		Examiner Name	J. Kennedy	
Sheet 2	2	of 2	Attorney Docket Number	MI22-2358

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
gu		Wolf et al., Silicon Processing for the VLSI Era, Volume 1-Process Technology, Second Edition, Lattice Press, 2000, pgs. 515-517.	
gu		Wolf et al., Silicon Processing for the VLSI Era, Volume 1-Process Technology, Second Edition, Lattice Press, 2000, pgs. 527-531.	

Examiner Signature	<i>J. Kennedy</i>	Date Considered	<i>July 22, 2005</i>
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